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Reducing Overhead When Using Loopback Cells for Fault Detection in

7.088,682

August 8, 2006

Balaji Lakshmikanth Bangolae

U.S. Patent No.

Issue Date:

Title:

First Named Inventor:

	Bi-Directional Virtual Circuits				
Art Unit:	2616				
Confirmation No.:	3956				
Examiner Name:	Christine Y. Ng				
Attorney Docket Number:	CSCO-009/119928				
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PTO/SB/96 (08-08)

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STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Cisco Technology, Inc.

Patent No. 7,	088,682	Issue Date:	August 8, 2006	
Entitled:	Reducing Overhead When Using Circuits	Loopback Cells for	Fault Detection in Bi-Directional Virtual	
Cisco Technology, Inc. Assignee, a California corporation, states that it is the assignee of the entire right, title, and interest, by virtue of an Assignment recorded on August 9, 2001, in the United States Patent and Trademark Office at Reel 012067, Frame 0887.				
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